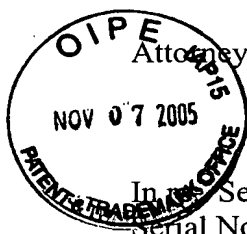


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Attorney Docket No.: 5649-1204

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Seong-Jin Jang
Serial No.: 10/780,057
Filed: February 17, 2004

Group No.: 2819
Examiner: Vibol Tan
Confirmation No. 9675

For: SEMICONDUCTOR DEVICES WITH REFERENCE VOLTAGE GENERATORS
AND TERMINATION CIRCUITS CONFIGURED TO REDUCE TERMINATION
MISMATCH

November 4, 2005

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

This Amendment is being filed in response to the Office Action mailed August 11, 2005 (hereinafter "Office Action").

It is not believed that any fee(s), including fees for additional claims, are required, beyond those that may otherwise be provided for in documents accompanying this paper. In the event, however, that additional fees are necessary to allow consideration of this paper, such an extension is also hereby petitioned for under 37 C.F.R. §1.136(a). Any additional fees believed to be due in connection with this paper may be charged to our Deposit Account No. 50-0220.